



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/828,340	04/05/2001	Niclas Wiberg	34645-00517USPT	1706
27045	7590	12/03/2004	EXAMINER	
ERICSSON INC. 6300 LEGACY DRIVE M/S EVR C11 PLANO, TX 75024				CHOU, ALBERT T
			ART UNIT	PAPER NUMBER
			2662	

DATE MAILED: 12/03/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)	
	09/828,340	WIBERG ET AL.	
	Examiner	Art Unit	
	Albert T. Chou	2662	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1) Responsive to communication(s) filed on 04-05-2001.
 2a) This action is FINAL. 2b) This action is non-final.
 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4) Claim(s) 7 is/are pending in the application.
 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
 5) Claim(s) 1-6, 14-45 is/are allowed.
 6) Claim(s) 7-13 is/are rejected.
 7) Claim(s) _____ is/are objected to.
 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9) The specification is objected to by the Examiner.
 10) The drawing(s) filed on 04-05-2001 is/are: a) accepted or b) objected to by the Examiner.
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)	4) <input type="checkbox"/> Interview Summary (PTO-413)
2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(s)/Mail Date. _____
3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date _____	5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)
	6) <input type="checkbox"/> Other: _____

DETAILED ACTION

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

1. Claims 7, 8, 9, 10, 11, 12 and 13 are rejected under 35 U.S.C. 102(b) as being anticipated by Zehavi (US Patent Number: 5,414,728).
2. Regarding claims 7 and 11, Zehavi teaches the method and means for generating a scrambling code sequence (Fig. 1) comprising receiving (Fig. 1; col. 4, lines 21-28) the output of exclusive-OR gate 110 (receiving a first scrambling code sequence), generating (Fig. 1; col. 4, lines 37-44) a pseudo-random sequence by PN_i generator 118 (generating a pseudo-random sequence) and combining (Fig. 1; col. 4, lines 44-47) the PN_i 118 signal with the output of exclusive-OR gate 110 to form the I-channel code spreading sequence 122 (combining the first scrambling code sequence and the pseudo-random sequence generated to for a second scrambling code sequence).
3. Regarding claims 8 and 12, *the step and means of being capable of generating a pseudo-random binary sequence* are inherent in Zehavi because the pseudo-random noise sequence generator PN_i 118 generates binary code sequence.
4. Regarding claim 9, *the pseudo-random binary sequence is a “+1” and “-1” sequence* is inherent in Zehavi. Zehavi discloses (col. 4, lines 47-50) that the I-channel

spread code sequence 122 and Q-channel code spread sequence 126 are used to bi-phase modulate a quadrature pair of sinusoids. For the modulation to occur, it is necessary to map the binary 1 and 0 valued chip to "+1" and "-1" valued chip during the modulation process.

5. Regarding claims 10 and 13, *the method and means of being capable of generating the pseudo-random sequence having half the rate of the first scrambling code sequence* are inherent in Zehavi. Zehavi discloses (col. 6, lines 5-7) that it is desirable that the chip rate be exactly divisible by the baseband data rates to be used in the system. It is also desirable for the divisor to be a power of two.

Allowable Subject Matter

6. Claims 1-6 and 14-45 are allowed.

7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Albert T. Chou whose telephone number is 571-272-6045. The examiner can normally be reached on 8:30 - 17:00. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Hassan Kizouo can be reached on 571-272-3088. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only.

Application/Control Number: 09/828,340
Art Unit: 2662

Page 4

For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

AC
Albert Chou

November 23, 2004

Chau T. Nguyen

CHAU NGUYEN
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2600